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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|----------|-------------|-------|----------|------|----------|
| 10016232 | 12/06/2001 | 716 | 2 | 2825 | J. |

**APPLICANTS: Donelly Ross; Naylor William; Fu Michael;

**CONTINUING DATA VERIFIED:

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** FOREIGN APPLICATIONS VERIFIED:

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| PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed 35 USC 119 conditions met | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no |
| Verified and Acknowledged Examiners's initials | ATTORNEY DOCKET NO SNSY-A2001-007 |
| TITLE : Multiple pass optimization for automatic electronic circuit placement | |

U.S. DEPT. OF COM/USPTO-436L (Rev. 12-94)

| NOTICE OF ALLOWANCE MAILED | | Primary Examiner | CLAIMS ALLOWED | |
|--|-----------|--------------------|----------------------|-----------------|
| Assistant | Attala | | Total Claims | Print Claim for |
| ISSUE FEE | | | DRAWING | |
| Amount Due | Date Paid | | Spec. Drwg. | Figs. Drwg. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | | Print Fig. | |
| | | PREPARED FOR ISSUE | Application Examiner | |
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